ES 250 2nd Midterm Exam - Fall 2013

Name k3

1. The switch in this circuit closes at time t = 0. Let v(0) denote the capacitor voltage when the switch is open and the circuit is at steady state. Similarly, let $v(\infty)$ denote the steady state capacitor voltage when the switch is closed.

Determine the values of v(0) and $v(\infty)$:

$$v(0) = _15_V \text{ and } v(\infty) = _24_V.$$





The values of the node voltages v_1 , v_2 and v_0 , are $v_1 = 80$ mV, $v_2 = -320$ mV and $v_0 = -960$ mV. Determine the value of the resistances R_1 , R_2 and R_3 :

 $R_1 = __40__k\Omega, R_2 = __15__k\Omega \text{ and } R_3 = __9__k\Omega.$

2.

3. Here's a circuit and its Thevenin equivalent circuit. Determine the values of the Thevenin resistance, R_t , and of the open-circuit voltage, V_{oc} .

$$R_{\rm t} = __35__\Omega$$
 and $V_{\rm oc} = __24__V$

4. Here's a circuit and its Thevenin equivalent circuit. Determine the values of the Thevenin resistance, R_t , and of the open-circuit voltage, V_{oc} .

$$R_{\rm t} = __{11.25}__{\Omega}$$
 and $V_{\rm oc} = __{52.5}__{V}$

5. Given that $0 \le R \le \infty$ in this circuit, and given these two observations:

When R = 0 then i = 1.5 A.

When $R = \infty$ then v = 24 V.

Fill in the blanks in the following statements:

- a) When $R = \underline{8} \Omega$ then v = 8 V.
- b) When $R = \underline{24} \Omega$ then i = 0.6 A.

6. Here are 4 separate dc circuits. Because they are dc circuits, the capacitors in these circuits act like open circuits and the inductors act like short circuits. Determine the values of i_1 , v_2 , v_3 and i_4 .

The equivalent circuit on the right is obtained from the original circuit on the left using source transformations and equivalent resistances. (The lower case letters *a* and *b* identify the nodes of the capacitor in both the original and equivalent circuits.) Determine the values of R_a , V_a , R_b and I_b in the equivalent circuit:

 $R_{a} = \underline{24} \Omega$, $V_{a} = \underline{-4} V$, $R_{b} = \underline{4.5} \Omega$ and $I_{b} = \underline{12} A$.

8. a) Determine the time constant, τ , and the steady state capacitor voltage, $v(\infty)$, when the switch is **open**:

$$\tau = __0.8$$
____s and $v(\infty) = __16$ ____V

b) Determine the time constant, τ , and the steady state capacitor voltage, $v(\infty)$, when the switch is **closed**:

$$\tau = 0.48$$
 s and $v(\infty) = 19.2$ V

9. The input to this circuit is the current: $i(t) = 5 + 2e^{-7t}$ A for t > 0The output is the voltage: $v(t) = 75 - 82e^{-7t}$ V for t > 0Determine the values of the resistance and inductance:

 $R = __15__\Omega$ and $L = __8__H$.

10. The input to this circuit is the voltage v_s . The output is the voltage v_o . The output is related to the input by the equation $v_o = m v_s + b$ where *m* and *b* are constants. The values of *m* and *b* are:

$$m = -5_V/V$$
 and $b = -9_V$.

11. This circuit is at steady state before the switch closes. The capacitor voltage can be represented as

$$v(t) = A + Be^{-at}$$
 V for $t > 0$

Determine the values of the real constants *A*, *B* and *a*:

 $A = __{12}$ V, $B = __{6}$ V and $a = __{20}$ 1/s.

Element Equations

First-Order Circuits

